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1. Field of the Invention The present invention relates to a semiconductor memory, and more particularly, to a semiconductor memory apparatus for semiconductor memory devices and methods of testing the semiconductor memory device. 2. Description of the Related Art When a semiconductor memory device is fabricated by semiconductor processes, a memory cell is formed by a capacitor and a metal-oxide-semiconductor (MOS) transistor. The capacitor, which is called a storage capacitor in the specification, stores charges, and the MOS transistor couples the storage capacitor and an internal data line. In a semiconductor memory device such as a dynamic random access memory (DRAM), random data is stored in the storage capacitor during fabrication. After fabrication, a test operation is performed to determine whether the semiconductor memory device operates normally. FIG. 1 illustrates the memory cell of a conventional semiconductor memory device. Referring to FIG. 1, a

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